

<b>Notice of References Cited</b>	Application/Control No. 10/669,722	Applicant(s)/Patent Under Reexamination WU ET AL.	
	Examiner Joseph P. Hirl	Art Unit 2129	Page 1 of 1

U.S. PATENT DOCUMENTS					
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	C	US-			
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NON-PATENT DOCUMENTS		
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	W	Chandu Visweswariah, Optimization Techniques for High-Performance Digital Circuits, 1997, IEEE, 198-205
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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